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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Eliyahou Harari et al.

Title: Multi-State Non-Volatile Integrated Circuit Memory Systems That Employ Dielectric Storage Elements

Application No.: 10/002,696 Filing Date: October 31, 2001

Examiner: Weiss, Howard Group Art Unit: 2814

Docket No.: SNDK.272US0 Conf. No.: 4652

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, Applicants call the document listed on the enclosed Form PTO-1449 to the Examiner's attention in this patent application. A copy of the document listed on the accompanying Form PTO-1449 is enclosed.

Citation of this document shall not be construed as (1) an admission that the document is prior art with respect to the invention or inventions claimed in this application, (2) a representation that a search has been made (other than as indicated by any cited document), or (3) an admission that the cited information is, or is considered to be, material to patentability as defined in § 1.56(b).

This information disclosure statement is submitted under 37 C.F.R. § 1.97(b) and consequently no fee should be required. The Commissioner is authorized, however, to charge

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any fee that may be required, or to credit any overpayment, against Deposit Account No. 502664. This form is being submitted in duplicate.

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Respectfully submitted,

Gerald P. Parsons May 14, 2004
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